

02.09.04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark Moyer; Jeffrey Byrne
Assignee: Lattice Semiconductor Corporation
Title: Continuous Self-Verify Of Configuration Memory In Programmable Logic Devices
Serial No.: 10/676,494 Filing Date 09/30/2003
Examiner: Unassigned Group Art Unit: 2124
Docket No.: M-15194 US

Irvine, California
February 3, 2004

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

REQUEST FOR A CORRECTED FILING RECEIPT

Dear Sir:

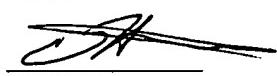
Attached is a copy of the official Filing Receipt received from the Patent and Trademark Office in the above-noted application for which issuance of a corrected filing receipt is respectfully requested.

The title should read as follows:

Continuous Self-Verify Of Configuration Memory In Programmable Logic Devices

Please telephone the undersigned at (949) 752-7040 if there are any questions.

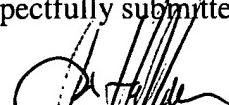
I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on



Eric Hoover

February 3, 2004
Date

Respectfully submitted,



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/676,494	09/30/2003	2124	990	M-15194 US	3	24	5

CONFIRMATION NO. 8571

Jon W. Hallman
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FILING RECEIPT



OC000000011569662

Date Mailed: 12/24/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Mark Moyer, Austin, TX;
 Jeffrey Byrne, Portland, OR;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/23/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Continuous self-verify configuration memory in programmable logic devices

Preliminary Class

708

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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